

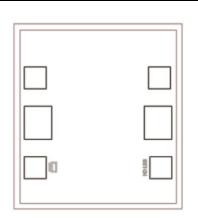
Gain Equalizer Die 50Ω 12dB DC to 20 GHz

#### **Product Features**

- 11.9 dB Slope
- Wide Bandwidth, DC-20 GHz
- Excellent Return Loss, 20 dB typ.

### **Typical Applications**

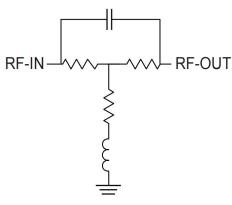
- Fixed Satellite
- Mobile
- Radio location
- · Space research



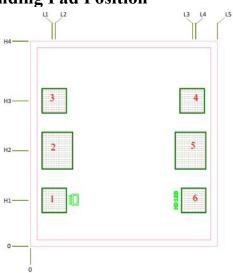
#### **General Description**

EQYT-12-24-D is an absorptive Gain Equalizer Die fabricated using highly repetitive GaAs IPD MMIC process incorporating resistors, capacitors and inductors having negative insertion loss slope. EQYT-12-24-D has a nominal attenuation slope of 11.9 dB.

### **Simplified Schematic and Pad description**



### **Bonding Pad Position**



| Pad Number | Function | Description   |  |  |
|------------|----------|---------------|--|--|
| 2          | RF-IN    | RF-Input pad  |  |  |
| 5          | RF-OUT   | RF-Output pad |  |  |
| 1,3,4,6    | GND      | Ground        |  |  |

| Dimensions in $\mu$ m, Typicai |     |     |     |     |     |     |     |     |           |          |                            |                    |
|--------------------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----------|----------|----------------------------|--------------------|
| L1                             | L2  | L3  | L4  | L5  | H1  | H2  | НЗ  | H4  | Thickness | Die Size | Bond Pad<br>#1, #3, #4, #6 | Bond Pad<br>#2, #5 |
| 95                             | 108 | 643 | 649 | 750 | 185 | 385 | 585 | 825 | 100       | 750x825  | 92 X 92                    | 117 X 142          |

Gain Equalizer Die  $50\Omega$  12dB DC to 20 GHz

# Electrical Specifications<sup>1</sup> at 25°C, $50\Omega$ , unless otherwise noted.

| Parameter       | Condition<br>(GHz)                       | Min.                  | Тур.                              | Max.                  | Units |
|-----------------|--|-----------------------|-----------------------------------|-----------------------|-------|
| Frequency Range |  | DC                    |                                   | 20                    | GHz   |
| Insertion Loss  | 0.01<br>5<br>10<br>18<br>20              | _<br>_<br>_<br>_<br>_ | 13.4<br>10.5<br>6.6<br>2.1<br>1.5 | _<br>_<br>_<br>_<br>_ | dB    |
| VSWR            | 0.01 - 5<br>5 - 10<br>10 - 18<br>18 - 20 | _<br>_<br>_           | 1.10<br>1.13<br>1.17<br>1.44      | _<br>_<br>_<br>_      | :1    |

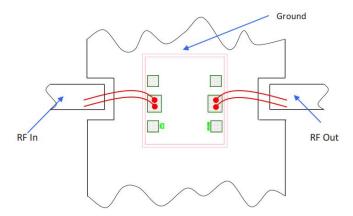
<sup>1.</sup> Measured on Die using MPI Titan series, 200 µm pitch GSG probe.

# Absolute Maximum Ratings<sup>2</sup>

| Operating Case<br>Temperature | -55°C to 105°C |
|-------------------------------|----------------|
| RF Input Power <sup>3</sup>   | 30 dBm         |

- 2. Permanent damage may occur if any of these limits are exceeded.
- 3. Derates linearly to 28 dBm at 105° C

## **Assembly Diagram**



#### **Assembly and Handling Procedure**

1. Storage

Dice should be stored in a dry nitrogen purged desiccators or equivalent.

2. ESD

MMIC GaAs Gain equalizer dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.

3. Die Attach

The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMatDM6030HK-PT/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.

4. Wire Bonding

Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.