

Product Features

- 2.1 dB Slope
- Excellent power handling 31 dBm
- Wide Bandwidth, DC-6 GHz
- Excellent Return Loss, 20 dB typ.

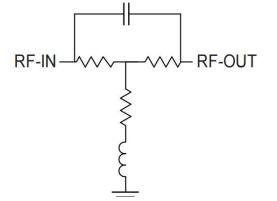
Typical Applications

- Cellular
- PCS
- Communications
- Radar
- Defense

General Description

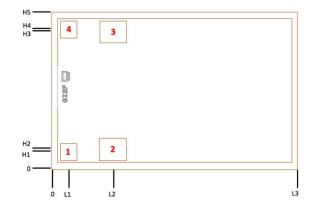
EQYT-2-63-D is an absorptive Gain Equalizer Die fabricated using highly repetitive GaAs IPD MMIC process incorporating resistors, capacitors and inductors having negative insertion loss slope. EQYT-2-63-D has a nominal attenuation slope of 2.1 dB.

Simplified Schematic and Pad description



Pad Number	Function	Description
2	RF-IN	RF-Input pad
3	RF-OUT	RF-Output pad
1,4 & Bottom of Die	GND	Ground

Bonding Pad Position



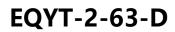
Dimensions in µm, Typical											
L1	L2	L3	H1	H2	H3	H4	H5	Thickness	Die Size	Bond Pad #1, #4	Bond Pad #2, #3
74	274	1107	74	86	613	626	700	100	1107x700	67 X 67	117 X 92

 Yantel Corporation

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 For detailed performance specs & shopping online see Yantel web site : www.yantel-corp.com



Gain Equalizer Die 50Ω 2dB DC to 6 GHz



EQYT-2-63-D

Gain Equalizer Die

50Ω 2dB DC to 6 GHz

Electrical Specifications¹ at 25°C, 50Ω, unless otherwise noted.

Parameter	Condition (GHz)	Min.	Тур.	Max.	Units
Frequency Range		DC		6	GHz
Insertion Loss	0.01 1 2 3 4 5 6		2.5 2.4 2.1 1.5 1.0 0.6 0.4		dB
VSWR	0.01 -1 1 - 2 2 - 3 3 - 4 4 - 5 5 - 6		1.02 1.03 1.04 1.08 1.17 1.29		:1

1. Measured on Yantel Characterization Test Board. Die is packaged in 2x2mm, 8-lead MCLP and soldered on testboard TB-1041-2-63+ See Characterization Test Circuit (Fig. 1)

Absolute Maximum Ratings²

Operating Case Temperature	-40°C to 85°C
RF Input Power	31 dBm

2. Permanent damage may occur if any of these limits are excedeed.

Characterization Test Circuit

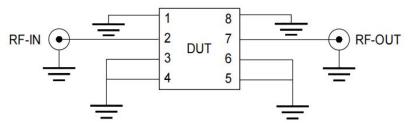


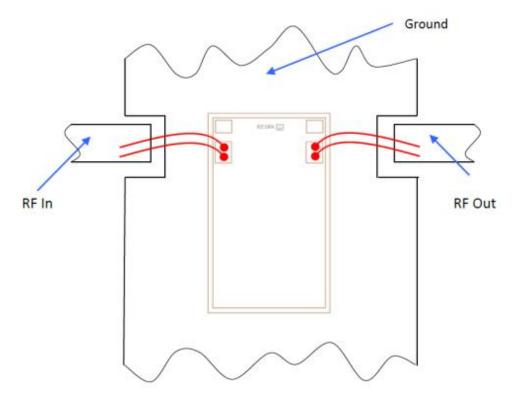
Fig 1. Block Diagram of Test Circuit used for characterization. Die is packaged in 2x2mm, 8-lead MCLP and soldered on testboard TB-1041-2-63+

Conditions: Attenuation & Return Loss Pin=0 dBm



EQYT-2-63-D Gain Equalizer Die 50Ω 2dB DC to 6 GHz

Assembly Diagram



Assembly and Handling Procedure

1. Storage

Dice should be stored in a dry nitrogen purged desiccators or equivalent.

2. ESD

MMIC GaAs Gain equalizer dice are susceptible to electrostatic and mechanical damage. Die are supplied inantistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.

3. Die Attach

The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030HK-PT/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.

4. Wire Bonding

Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.